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For more than 40 years, Computerworld has been the leading source of technology news and information for IT influencers worldwide. Computerworld's award-winning Web site (Computerworld.com), twice-monthly publication, focused conference series and custom research form the hub of the world's largest global IT media network.

A highly personal account of a remarkable journey that pushed the author to the brink

In 2007 and 2008, the world witnessed a dramatic increase in food prices. The global financial crisis that began in 2008 compounded the burden of high food prices, exacerbating the problems of hunger and malnutrition in developing countries. The tandem food price and economic crises struck amidst the massive, chronic problem of hunger and undernutrition in developing countries. National governments and international actors have taken a variety of steps to mitigate the negative effects of increased food prices on particular groups. The recent abrupt increase in food prices, in tandem with the current global economic crisis, threatens progress already made in these areas, and could inhibit future efforts. The Institute of Medicine held a workshop, summarized in this volume, to describe the dynamic technological, agricultural, and economic issues contributing to the food price increases of 2007 and 2008 and their impacts on health and nutrition in resource-poor regions. The compounding effects of the current global economic downturn on nutrition motivated additional discussions on these dual crises, their impacts on the nutritional status of vulnerable populations, and opportunities to mitigate their negative nutritional effects.

Architecture Design for Soft Errors provides a comprehensive description of the architectural techniques to tackle the soft error problem. It covers the new methodologies for quantitative analysis of soft errors as well as novel, cost-effective architectural techniques to mitigate them. To provide readers with a better grasp of the broader problem definition and solution space, this book also delves into the physics of soft errors and reviews current circuit and software mitigation techniques. There are a number of different ways this book can be read or used in a course: as a complete course on architecture design for soft errors covering the entire book; a short course on architecture design for soft errors; and as a reference book on classical fault-tolerant machines. This book is recommended for practitioners in semi-conductor industry, researchers and developers in computer architecture, advanced graduate seminar courses on soft errors, and (iv) as a reference book for undergraduate courses in computer architecture. Helps readers build-in fault tolerance to the billions of microchips produced each year, all of which are subject to soft errors Shows readers how to quantify their soft error reliability Provides state-of-the-art techniques to protect against soft errors

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